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March 25, 2004

Time: 2:14 pm
(Minneapolis, Minn.)TO: Commissioner for Patents
Attn: Ly D. Pham
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Facsimile Center
P.O. Box 1450
Alexandria, VA 22313-1450FROM: Timothy B. CliseOUR REF: 303.729US1TELEPHONE: 571-272-1793FAX NUMBER 571-273-1793

* Please deliver to Examiner Ly D. Pham in Art Unit 2818. *

Document(s) Transmitted: A Supplemental Amendment (8 pages).Total pages of this transmission, including cover letter: 9 pgs.

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In re. Patent Application of: Ramandeep S. SawhneyExaminer: Ly D. PhamSerial No.: 09/881472Group Art Unit: 2818Filed: June 14, 2001Docket No.: 303.729US1Title: SEMICONDUCTOR MEMORY WITH WORDLINE TIMING

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S/N 09/881,472PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Ramandeep S. Sawhney

Examiner: Ly D. Pham

Serial No.: 09/881,472

Group Art Unit: 2818

Filed: June 14, 2001

Docket No.: 303.729US1

Title: SEMICONDUCTOR MEMORY WITH WORDLINE TIMING

SUPPLEMENTAL AMENDMENT

Commissioner for Patents
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Alexandria, VA 22313-1450

This paper supplements the Response and Amendment filed December 17, 2004. Please
amend the above-identified patent application as follows.